Claim 1 (twice amended):

1.   A universal testing module, capable of connecting to a computer having communication
ports to be tested and forming communication paths through said ports, for testing the
condition of each pin of a parallel port and a serial port of said communication ports, said
universal testing module comprising:

a logic control unit, having at least a pair of input/output ports for communicating with
said parallel port;

a memory unit for storing instructions for controlling said logic control unit and said
computer and for temporary exchange of data; and an universal asynchronous combined
receiver and transmitter; and

a voltage converter for voltage interchange of RS-232 and TTL and enabling said logic
control unit to communicate with said computer through said serial port and executing a
testing procedure by comparing input and output signals.